

Chip Design Verification Engineer

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Company: NVIDIA

Location: Roskilde

Category: architecture-and-engineering

We are looking to hire a Chip Design Verification Engineer to join NVIDIA Chip Design group. The work environment is versatile, educational, dynamic and challenging as our employees are currently working on innovative, next-generation networking devices at the forefront of technology in terms of performance and power efficiency. Daily work involves all aspects of chip development: Design, Micro- Architecture, Firmware, and Verification. Work with the best and become one of the best!

What you will be doing:

Work in a combined design and verification team which develops core units within the Networking silicon.

Build reference models, verify and simulate chip blocks/entities according to specifications and performance requirements.

Work closely with multiple teams within organizations such as Architecture, Micro-Architecture, FW and Post-Silicon validation.

What we need to see:

B.SC./ M.SC. in Computer Engineering /Electrical Engineering/Communication Engineering or equivalent experience

2+ years of proven experience in RTL FrontEnd Asic Design or Verification (Chip Design)

Deep knowledge in HDL (Verilog/VHDL)

High Level English

Ways to stand out from the crowd:

Validated experience in ASIC Verification.

Knowledge in Specman.

Knowledge in SimVision

NVIDIA has some of the most forward-thinking and hardworking people in the world working for us. Are you creative and autonomous? Do you love the challenge of crafting the highest performance & lowest power silicon possible? If so, we want to hear from you. Come, join our Networking Silicon design team and help us build the next chip in this exciting and quickly growing field.

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